

FIG. 1

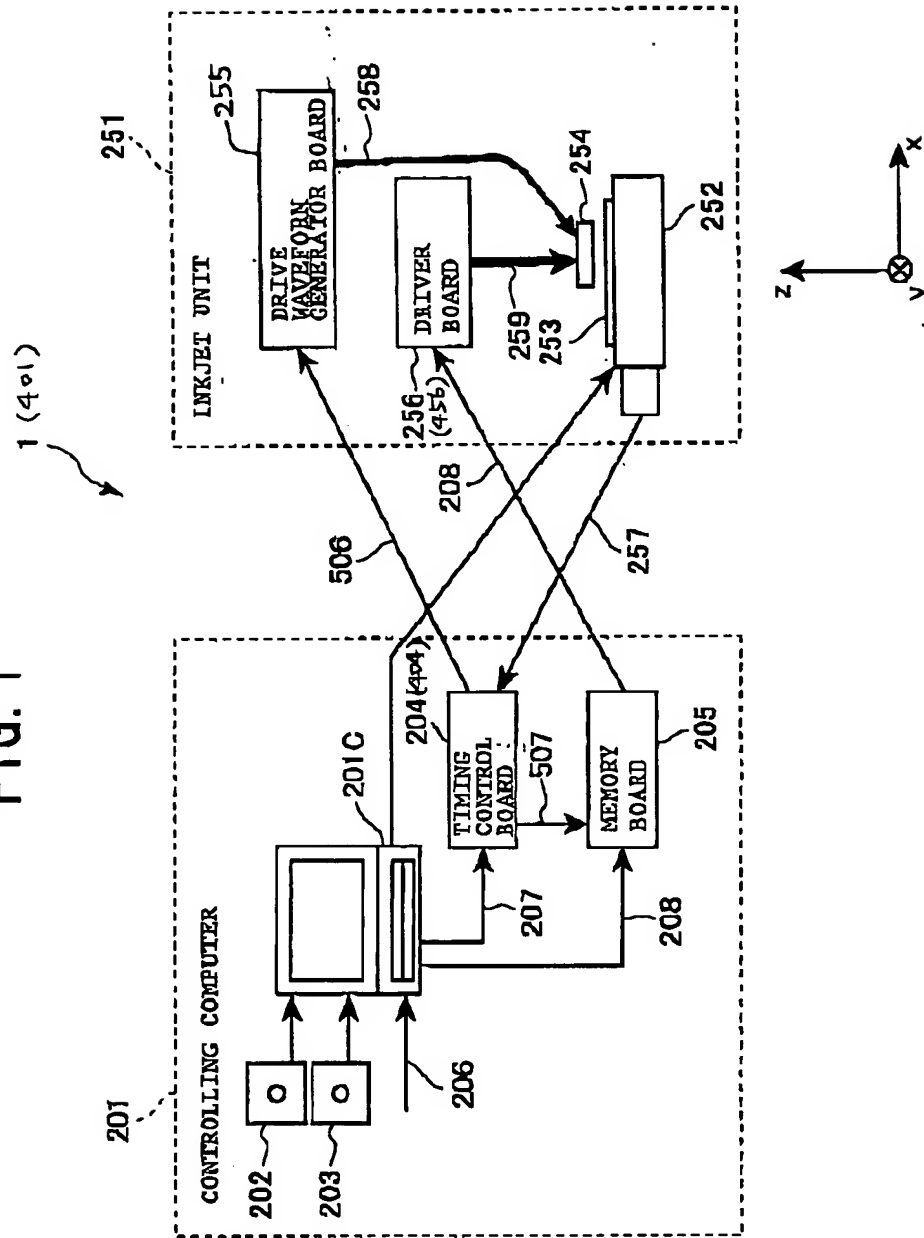


FIG. 2

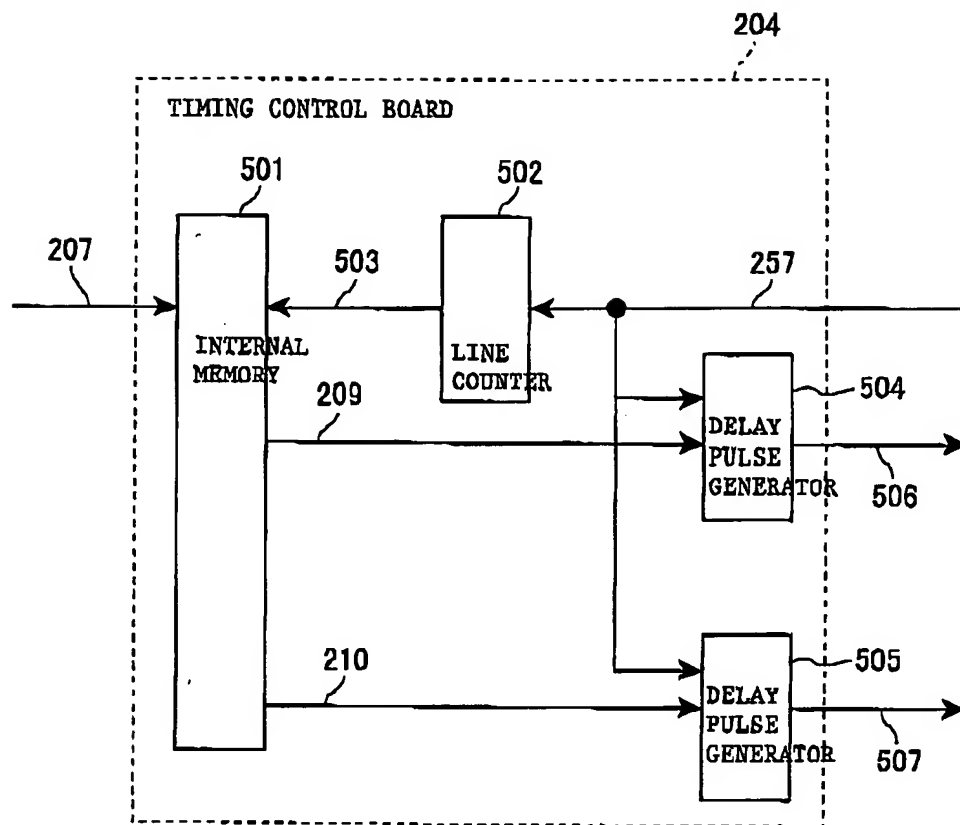


FIG. 3

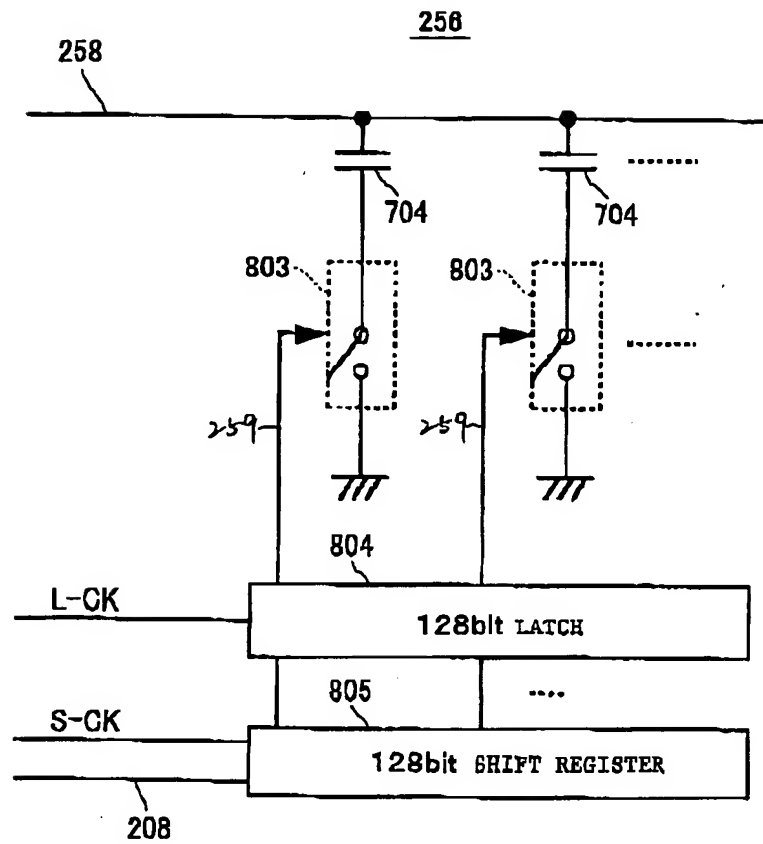


FIG. 4

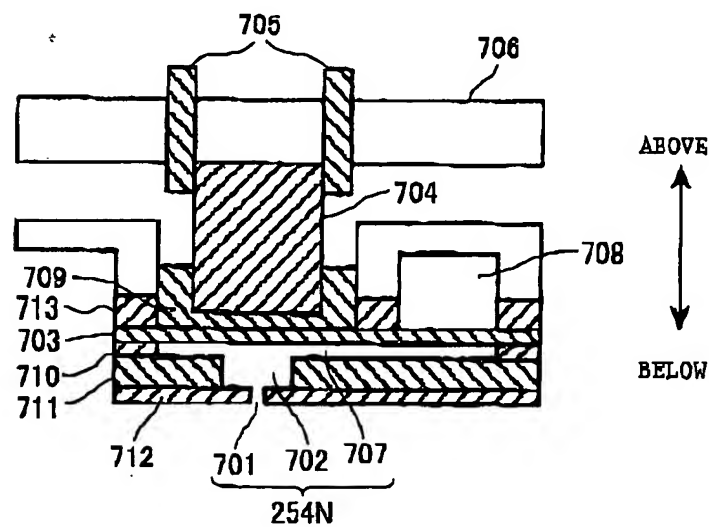


FIG. 5(1)

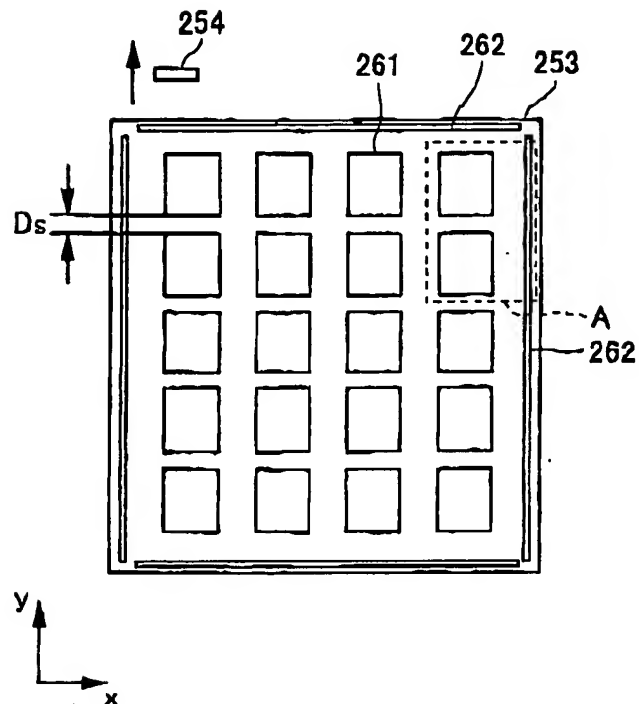


FIG. 5(2)

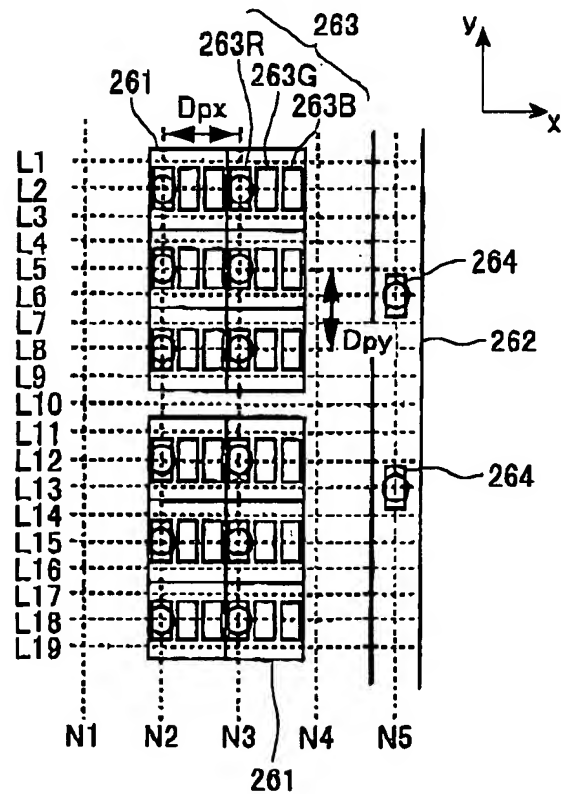


FIG. 6

207

257 209 210

206

y

x

208

		N1	N2	N3	N4	N5
L1	0 0					
L2	1 1	0	1	1	0	0
L3	0 0					
L4	0 0					
L5	1 0					
L6	1 1	0	0	0	0	1
L7	0 0					
L8	1 1	0	1	1	0	0
L9	0 0					
L10	0 0					
L11	0 0					
L12	1 0					
L13	1 1	0	0	0	0	1
L14	0 0					
L15	1 1	0	1	1	0	0
L16	0 0					
L17	0 0					
L18	1 0					
L19	0 0					

FIG.7(1)

0100 1101 0001 1010 010 ~ 207
0100 0101 0000 1010 000 ~ 209
208
(L2)01100
(L6)00001
(L8)01100
(L13)00001
(L15)01100

TOTAL OF 63 BIT

FIG. 7(2)

(L1)00000
(L2)01100
(L3)00000
(L4)00000
(L5)01100
(L6)00001
(L7)00000
(L8)01100
(L9)00000
(L10)00000
(L11)00000
(L12)01100
(L13)00001
(L14)00000
(L15)01100
(L16)00000
(L17)00000
(L18)01100
(L19)00000

TOTAL OF 95 BIT

FIG.8

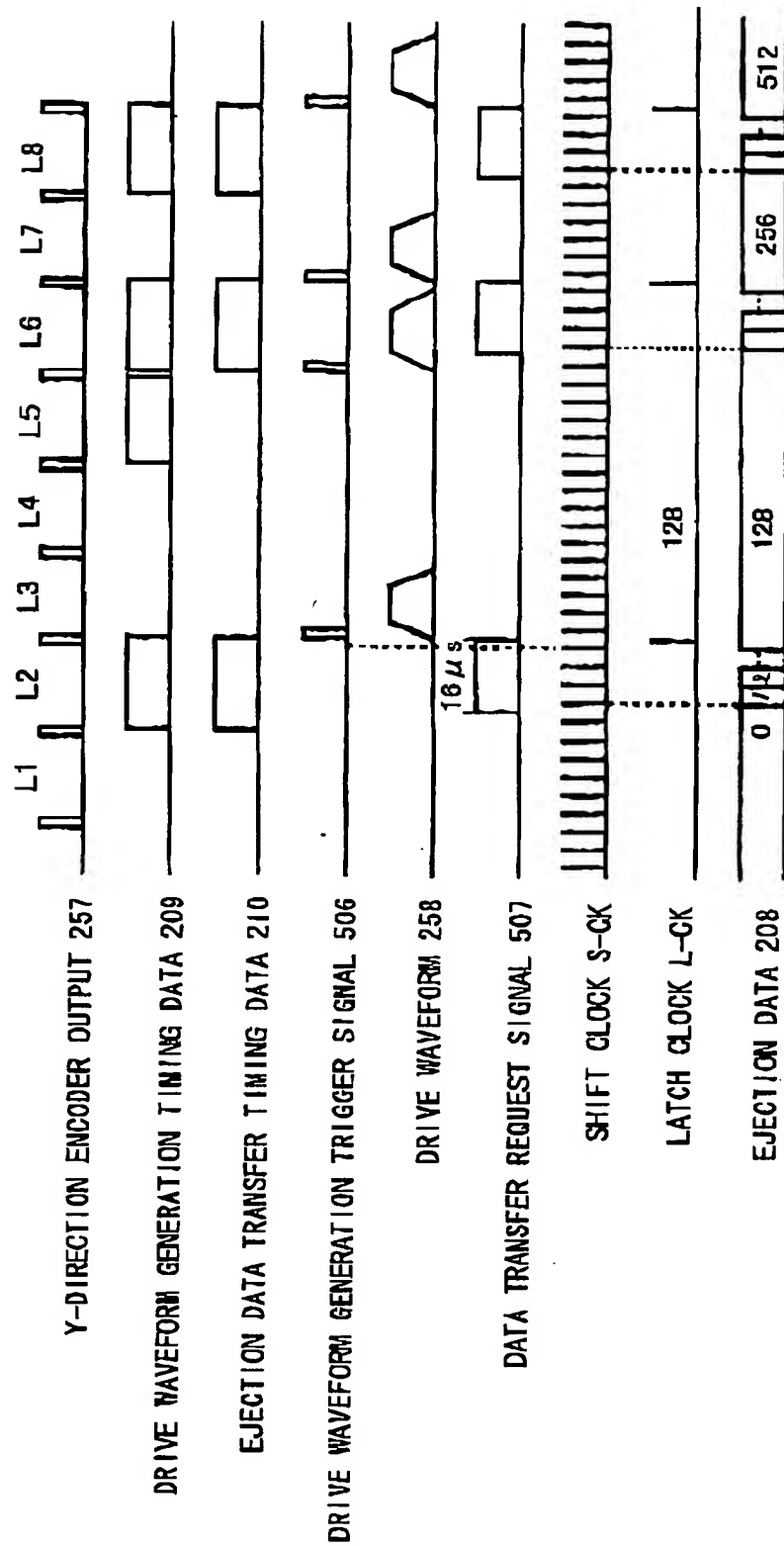


FIG. 9

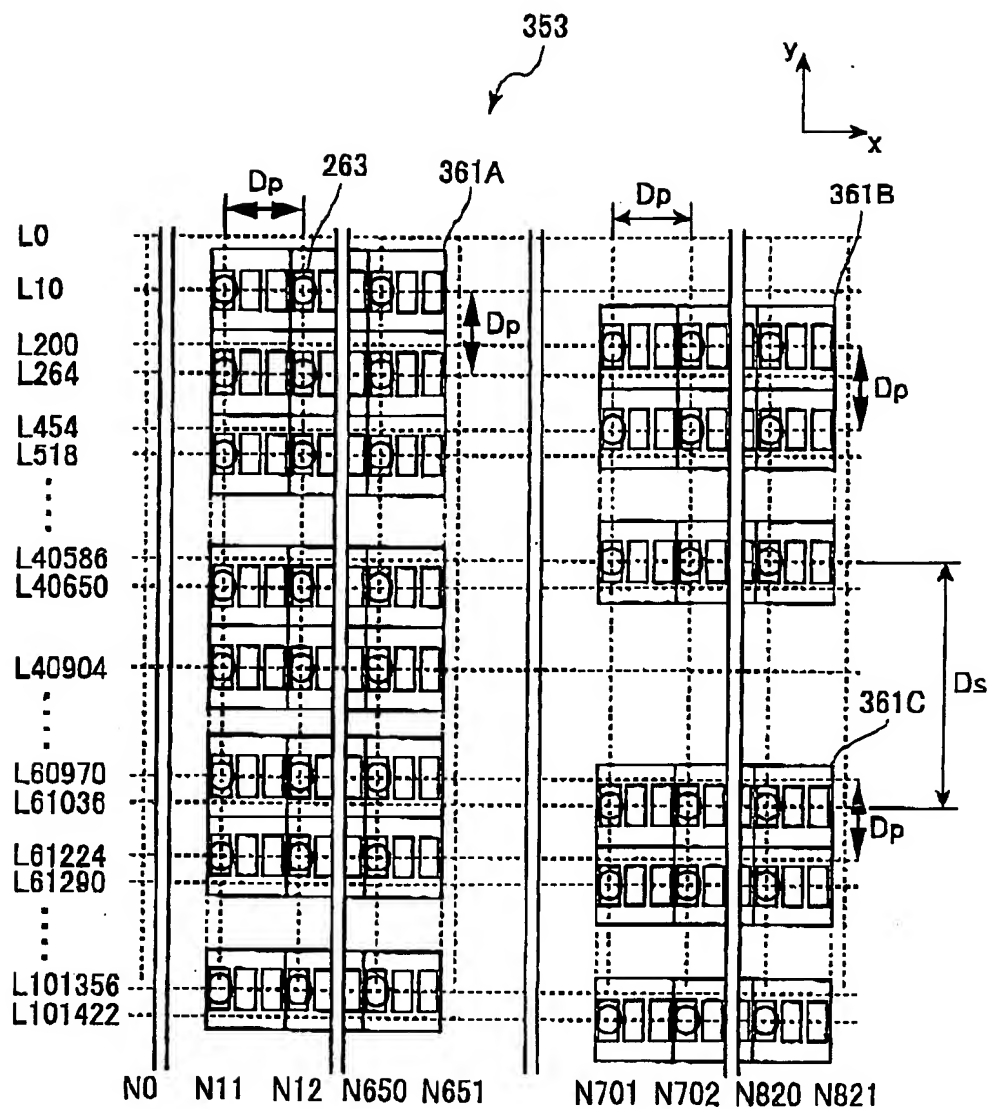


FIG. 10

FIG. 10 is a schematic diagram illustrating a data structure or memory layout. The diagram features a grid of data points, with labels 207, 257, 209, 210, 306, 208, 361B, 361A, and 361C. A coordinate system with x and y axes is shown in the upper right corner.

The data is organized into rows and columns. The rows are labeled on the left side, and the columns are labeled at the top. The data points are represented by binary values (0s and 1s) within the grid cells.

	257	207	209	210	306	N0	N11	N12	N650	N651	N701	N702	N820	N821	208
L0		0	0												
L10	1	1	0	1	1	0	1	1	1	0	0	0	0	0	
L200	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
L264	1	1	0	1	1	1	1	1	0	0	0	0	0	0	
L454	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
L518	1	1	0	1	1	1	1	1	0	0	0	0	0	0	
L40586	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
L40650	1	1	0	1	1	1	1	1	0	0	0	0	0	0	
L40904	1	0													
L60970	1	0													
L61036	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
L61224	1	1	0	1	1	1	1	1	0	0	0	0	0	0	
L61290	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
L101356	1	1	0	1	1	1	1	1	0	0	0	0	0	0	
L101422	1	1	0	0	0	0	0	0	0	0	1	1	1	0	
S	0	0													

Labels 361A, 361B, and 361C are positioned below the grid, indicating specific data points or regions. Label 361A is located below the first column of data points, 361B is located below the second column, and 361C is located below the third column.

FIG.11

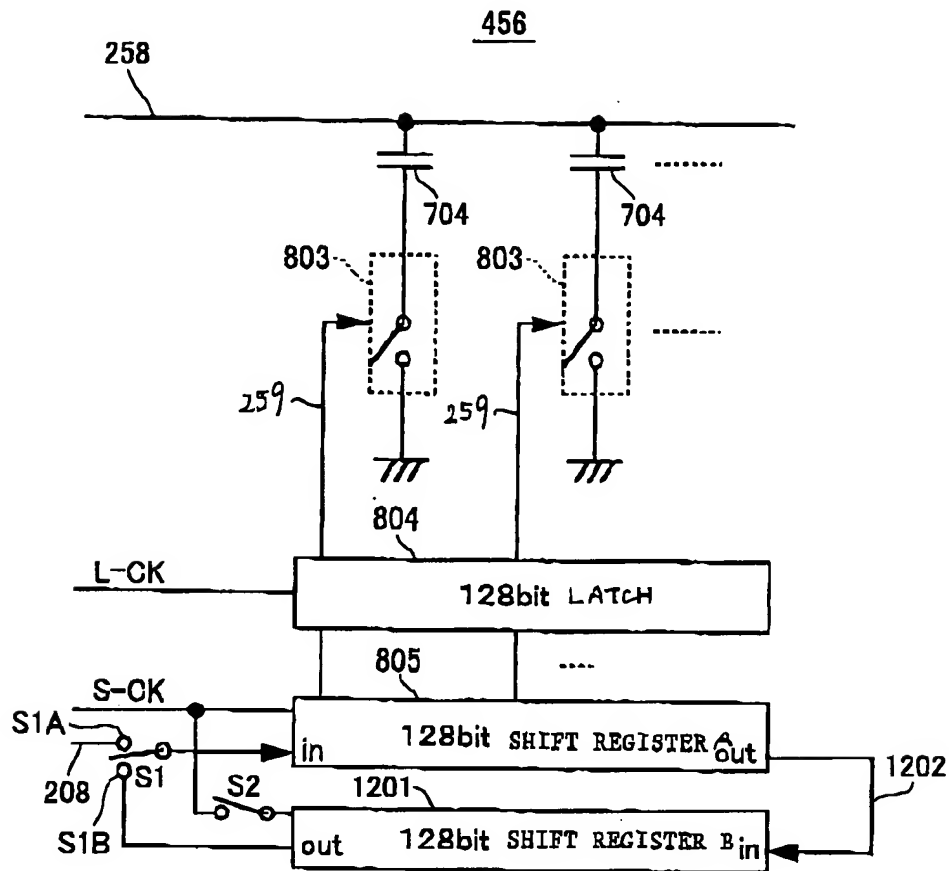


FIG.13

		M0	M1	M2	M3	M4
407	1101	DRIVE WAVEFORM GENERATION	0	1	1	1
	1102	COATING DATA TRANSFER	0	0	0	1
	1103	DATA ROTATION	*	0	1	0
		LATCH CLOCK L-CK	0	0	1	1
		SHIFT CLOCK S-CK	0	0	1	1
408	1104	SWITCH S1	*	*	S1B	S1A
	1105	SWITCH S2	*	*	CLOSE	OPEN

FIG. 12

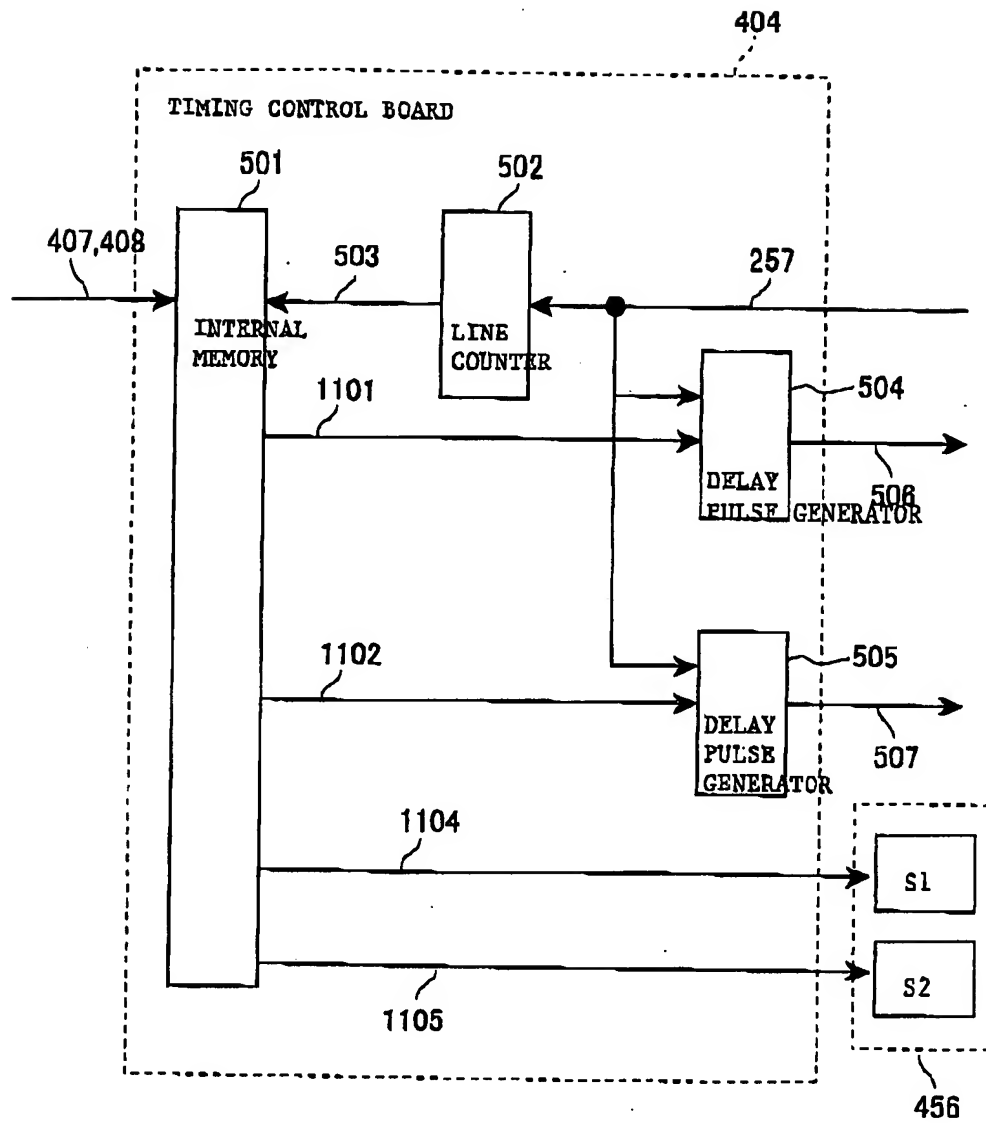


FIG. 14

